

# Call for Papers



## IEEE International NEWCAS Conference June 26 to 29, 2016 Vancouver, BC, Canada

### General Program Co-Chairs

Mohammad Sawan, Polytechnique Montréal  
Shahriar Mirabbasi, University of British Columbia

### Technical Program Co-Chairs

Ljiljana Trajkovic, Simon Fraser University  
Sudip Shekhar, University of British Columbia  
Luc Hébrard, University of Strasbourg

### Tutorial Co-Chairs

Patricia Desgreys, Telecom ParisTech  
Jean-Baptiste Begueret, University of Bordeaux  
Benoit Gosselin, Université Laval

### Special Session Co-Chairs

Thierry Taris, University of Bordeaux  
Carlos Saavedra, Queen's University  
Roman Genov, University of Toronto

### Publicity Co-Chairs

Zhihua Wang, Tsinghua University  
Ricardo Reis, Federal University of Rio Grande do Sul  
Magdy Bayoumi, University of Louisiana

### Industry Liaison

Peter Stokes, CMC Microsystems  
Bob Gill, British Columbia Institute of Technology

### Media/Web Site Chair

Stephen Makonin, Simon Fraser University

The 14<sup>th</sup> IEEE International NEWCAS Conference will be held, jointly with FTFC 2016, in Vancouver, BC, Canada from June 26 to 29, 2016. from June 26 to 29, 2016. The venue for the conference is the Vancouver Marriott Pinnacle Downtown Hotel, which is just steps away from Coal Harbour, the famous Vancouver waterfront, and many other exciting attractions.

The program of the conference will be tailored to reflect the wide spectrum of topics and research interests shared among the researchers and practitioners in the field of circuits and systems and to provide all attendees with a unique forum for the exchange of ideas and results. It will also encompass a wide range of tutorials, special sessions and keynote talks given by prominent experts covering key areas of research in microsystems.

The topics include, but are not limited to:

|                                    |                               |
|------------------------------------|-------------------------------|
| Analog/mixed-signal circuits       | Energy harvesting circuits    |
| Biomedical circuits and systems    | Imaging and image sensors     |
| CAD and design tools               | Low-power low-voltage designs |
| Communication circuits and systems | Microsystems                  |
| Computer architecture and memories | Neural networks               |
| Digital circuits                   | Sensory circuits and systems  |
| Digital signal processing          | Technology Trends             |
| Embedded systems                   | Test and verification         |

For detailed information on proposal and paper submission procedure please refer to the conference website: <http://newcas2016.ieee.ca>

Only electronic submissions will be accepted. Complete 4-page papers (in standard IEEE double-column conference format) should be submitted in PDF format that is IEEE Xplore compliant. Accepted technical and special session papers will appear in IEEE Xplore digital library.

### Submission schedule:

### Submission deadline Notification of acceptance

|                           |                   |                  |
|---------------------------|-------------------|------------------|
| Special session proposals | January 25, 2016  | February 1, 2016 |
| Tutorial proposals        | January 25, 2016  | February 1, 2016 |
| Full paper submission     | February 28, 2016 | April 11, 2016   |
| Final manuscript          | April 30, 2016    |                  |

Authors of selected number of high quality papers will be invited to submit an extended version of their work to the Springer Journal of Analog Integrated Circuits and Signal Processing, Special Issue on NEWCAS 2016.

